

L Number	Hits	Search Text	DB	Time stamp
-	0	09265119.an.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:25
-	131	(PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:27
-	51	((PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.) and (eprom or eeprom)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:27
-	34	((((PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.) and (eprom or eeprom)) and flash	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:27
-	10	(((((PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.) and (eprom or eeprom)) and flash) and ("nor"))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:27
-	10	((((((PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.) and (eprom or eeprom)) and flash) and ("nor")) and memor\$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 21:11
-	1	5559449.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 17:59
-	376	("nor" adj flash) with memor\$4	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 18:00
-	216	((("nor" adj flash) with memor\$4) and (eprom or eeprom)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 18:00
-	71	((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:08
-	23	((((((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector) and block and page) and ((address\$2 with bus\$3) or (address\$2 with counter\$1))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 18:46
-	34	((((((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector) and block and page	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 19:03
-	1	5682497.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 19:19
-	1	5682497.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 19:19
-	1	((((((PERI-MAURIZIO BRIGATI-ALESSANDRO OLIVO-MARCO).in.) and (eprom or eeprom)) and flash) and ("nor")) and memor\$3 and pointer	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 21:20
-	1	5966723.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/20 21:20
-	20	((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector and (controller or microcontroller or cpu)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:09

-	14	((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector and (controller or microcontroller)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:09
-	2	((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and sector and (microcontroller)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:12
-	19	((("nor" adj flash) with memor\$4) and (eprom or eeprom)) and (microcontroller)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:14
-	24	((("nor" adj flash)) and (eprom or eeprom)) and (microcontroller)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:52
-	1	5966723.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:54
-	1	5956268.pn.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/21 12:54

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**1 A 32-bit RISC microcontroller with 448K bytes of embedded flash memory**

Kuo, C.; Chrudimsky, D.; Jew, T.; Gallun, C.; Choy, J.; Wang, B.; Pessoney, S.; Choe, H.; Harrington, C.; Eguchi, R.; Strauss, T.; Prinz, E.; Swift, C.; Nonvolatile Memory Technology Conference, 1998. 1998 Proceedings. Seventh Biennial IEEE, 22-24 June 1998  
 Pages:28 - 33

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